

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hisakazu DATE

Appln. No.:

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT

* * *

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

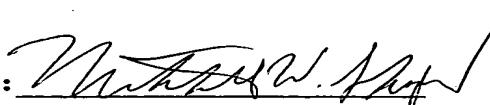
Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the document listed on the attached Form PTO-1449 is hereby cited.

The document on the attached List was cited in the specification, on page 3, and its relevance is indicated therein.

Respectfully submitted,

MWS:lat

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By: 
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March 24, 2004

FORM PTO-1449				Atty. Docket No. XA-10062	Appln. No.		
<u>LIST OF DOCUMENTS CITED BY APPLICANT</u>				Applicant Hisakazu DATE			
				Filing Date HEREWITH	Group		
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Country	Class	Sub-class	Translation
	AJ	2002-76123	3/15/02	Japan			Abstract
	AK						
	AL						
	AM						
	AN						
	AO						
OTHER (including author, title, date, pertinent pages, etc.)							
	AP						
	AQ						
	AR						
Examiner				Date Considered			
XAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							